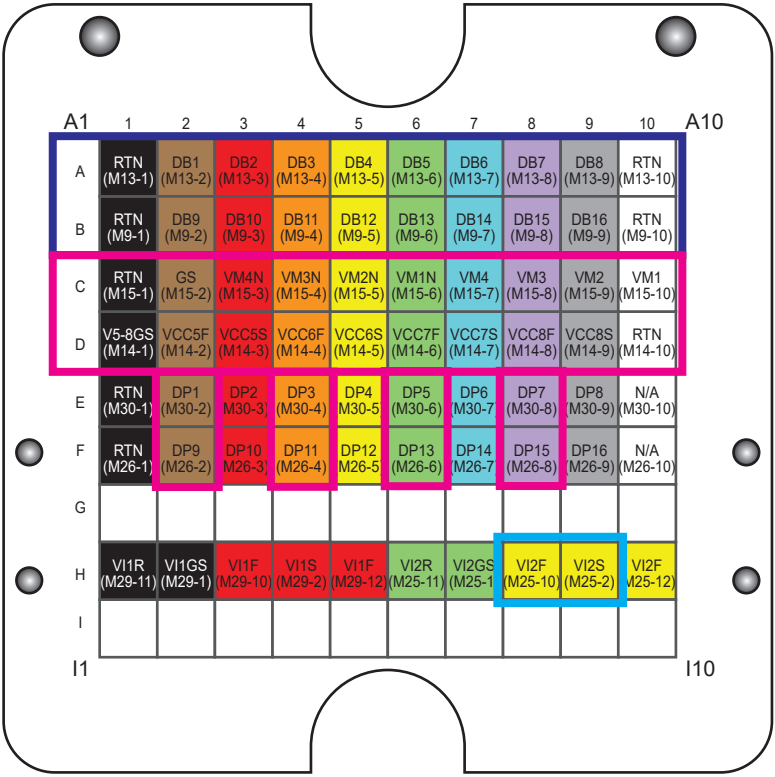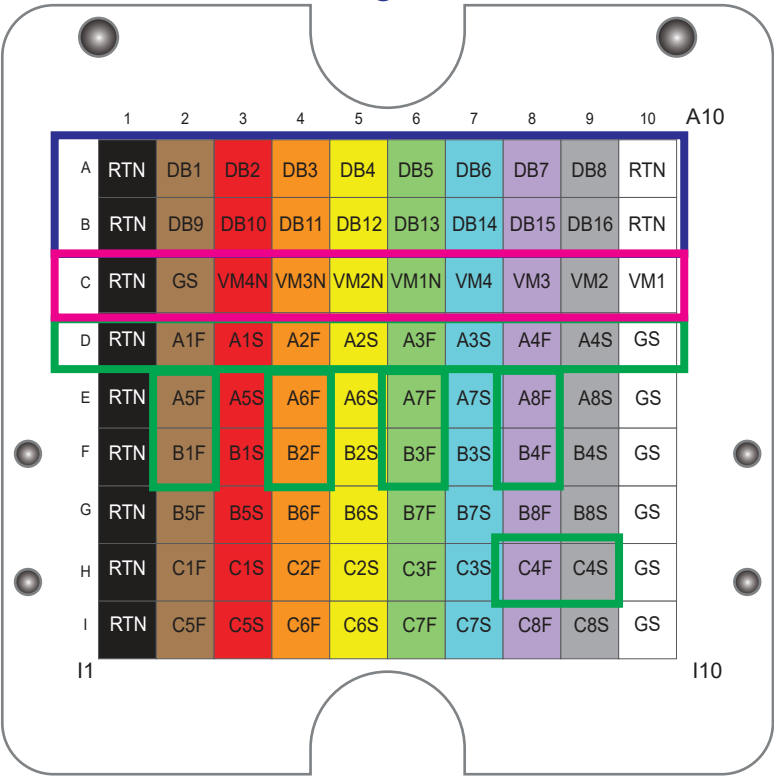# RI8546A, DevicePower
DutControl, StaticDigital, PowerVI

RI8612A, DevicePowerPlus#
DutControlPlus#, DevicePowerPlus#
StaticDigitalPlus#

RI8546A pin map

	A1 1	2	3	4	5	6	7	8	9	10 A10
A	RTN (M13-1)	DB1 (M13-2)	DB2 (M13-3)	DB3 (M13-4)	DB4 (M13-5)	DB5 (M13-6)	DB6 (M13-7)	DB7 (M13-8)	DB8 (M13-9)	RTN (M13-10)
B	RTN (M9-1)	DB9 (M9-2)	DB10 (M9-3)	DB11 (M9-4)	DB12 (M9-5)	DB13 (M9-6)	DB14 (M9-7)	DB15 (M9-8)	DB16 (M9-9)	RTN (M9-10)
C	RTN (M15-1)	GS (M15-2)	VM4N (M15-3)	VM3N (M15-4)	VM2N (M15-5)	VM1N (M15-6)	VM4 (M15-7)	VM3 (M15-8)	VM2 (M15-9)	VM1 (M15-10)
D	V5-8GS (M14-1)	VCC5F (M14-2)	VCC5S (M14-3)	VCC6F (M14-4)	VCC6S (M14-5)	VCC7F (M14-6)	VCC7S (M14-7)	VCC8F (M14-8)	VCC8S (M14-9)	RTN (M14-10)
E	RTN (M30-1)	DP1 (M30-2)	DP2 (M30-3)	DP3 (M30-4)	DP4 (M30-5)	DP5 (M30-6)	DP6 (M30-7)	DP7 (M30-8)	DP8 (M30-9)	N/A (M30-10)
F	RTN (M26-1)	DP9 (M26-2)	DP10 (M26-3)	DP11 (M26-4)	DP12 (M26-5)	DP13 (M26-6)	DP14 (M26-7)	DP15 (M26-8)	DP16 (M26-9)	N/A (M26-10)
G										
H	VI1R (M29-11)	VI1GS (M29-1)	VI1F (M29-10)	VI1S (M29-2)	VI1F (M29-12)	VI2R (M25-11)	VI2GS (M25-1)	VI2F (M25-10)	VI2S (M25-2)	VI2F (M25-12)
I										I10

RI8612A pin map

	1	2	3	4	5	6	7	8	9	10 A10
A	RTN	DB1	DB2	DB3	DB4	DB5	DB6	DB7	DB8	RTN
B	RTN	DB9	DB10	DB11	DB12	DB13	DB14	DB15	DB16	RTN
C	RTN	GS	VM4N	VM3N	VM2N	VM1N	VM4	VM3	VM2	VM1
D	RTN	A1F	A1S	A2F	A2S	A3F	A3S	A4F	A4S	GS
E	RTN	A5F	A5S	A6F	A6S	A7F	A7S	A8F	A8S	GS
F	RTN	B1F	B1S	B2F	B2S	B3F	B3S	B4F	B4S	GS
G	RTN	B5F	B5S	B6F	B6S	B7F	B7S	B8F	B8S	GS
H	RTN	C1F	C1S	C2F	C2S	C3F	C3S	C4F	C4S	GS
I	RTN	C5F	C5S	C6F	C6S	C7F	C7S	C8F	C8S	GS

NOT Backware compatible with RI8546 TIMs (testplan will not compile or run)

But, with testplan modifications, the following limited compatibility is achievable.

Static Digital is 100% compatible
 Rows A,B are equivalent hardware pins and software buttons (i.e. owner changes from "StaticDigital" to "StaticDigitalPlus")
DutControl 70% compatible. (28 / 40 pins compatible)
 Row C - VMs are 100% identical with equivalent hardware pins and new software buttons added (owner changes)
 Row D - VCC## 100% identical pins with different software buttons added (owner changes)
 Row E,F - DP# are 50% identical, only ODD numbered pins "DP1, DP3, ... , DP15" ONLY with internal sense (limited hardware performance).
PowerVI 20% compatible (2/10 pins):
 Pins H8 & H9 - supported with different buttons/testplan changes required, limited hardware performance